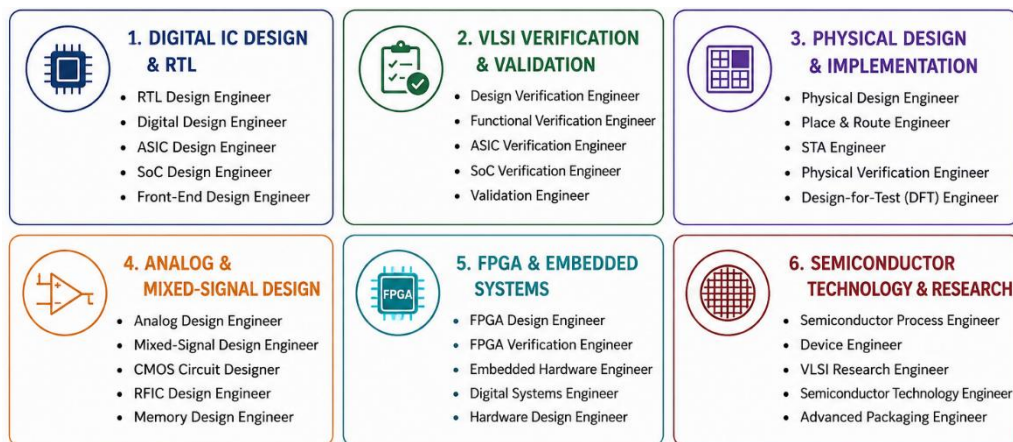


HOW THIS PROGRAMME BENEFITS YOU:

- **Master VLSI Technologies:** Build expertise in semiconductor and VLSI design.
- **Design Intelligent Hardware:** Integrate VLSI, embedded systems, and AI.
- **Develop Industry Skills:** Gain hands-on experience with modern EDA tools and design methodologies.
- **Build Career Readiness:** Prepare for careers in semiconductors, VLSI, embedded systems, and R&D.
- **Innovate for the Future:** Develop research, problem-solving, and innovation skills.

CAREER PATHWAYS:



OUR PAST RECRUITERS:

- MosChip Technologies Ltd.
- Tessolve Semiconductors Pvt Ltd.
- Dynolt Technologies Pvt. Ltd.
- Vaaluka Solutions Pvt. Ltd.
- Renault Nissan R&D
- Inerve Power Solutions Pvt. Ltd.
- Microchip
- Infosys
- Medha Servo Drives Pvt .Ltd.
- Capgemini (VLSI)
- Cisco
- Hyundai Mobis
- Soctronics Technologies Pvt. Ltd.
- TCS

ROLES OF DISTINGUISHED ALUMNI

- Signal Engineer in IRS, Govt. of India
- Scientists in DRDO, NSTL, ISRO, BARC.
- Technical Leads in INTEL(USA), SAMSUNG R&D, Analog Devices
- Design Engineers in CADENCE Design Systems, AMD, BROADCOM R&D, Texas Instruments
- R&D Engineer in SYNOPSIS
- Founder and CEO, MARS INNO India Pvt. Ltd.
- Faculty & Researchers at leading institutions like IIT-Delhi, IIT-Madras, IIT-Hyderabad, IIT Mandi.

GVP - Estd.1988



GVPIHLR - Estd.2026

GAYATRI VIDYA PARISHAD
INSTITUTE OF HIGHER LEARNING AND RESEARCH
(Deemed to be University under Distinct Category
under Section 3 of the UGC Act, 1956)
Kommadi, Madhurawada, Visakhapatnam – 530 048, Andhra Pradesh

School of Electrical and Electronics Engineering

Department of Electronics and Communication Engineering

B.Tech. Electronics Engineering (VLSI Design & Technology) (GVPIHLR-Main Campus, Kommadi) Brochure

Vision : The vision of Electronic and Communications Engineering Department is to be in the lead to create and develop professional and intellectual human capital in electronics and communication engineering and applications in order to foster the technological, economic and social enrichment of the state and the nation and to contribute to global village connectivity

Mission : To play professional role to create, develop, organize and manage complex technologies and products, contribute to the betterment of society and evolve better quality of living in a world increasingly influenced by scientific and technological innovation.

To provide students of E & C Engineering an environment of academic freedom that will ensure the exchange of ideas and the dissemination of knowledge in this discipline.

To Recognize as a place that encourages research excellence and diversity in thought and endeavour in multidisciplinary applications.

Contact details:

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ABOUT GVPIHLR:

Gayatri Vidya Parishad (GVP) is established in 1988 with a service motive, committed to develop education in India. The basic aim is

“to promote, establish, maintain and assist or aid in maintaining educational and other institutions with a view to impart education in all branches in general and in Advanced Technology, Management, Education and Science (including Healthcare) in particular and thus to facilitate securing gainful employment for the youth of the country.”

In line with this and in tune with National Educational Policy 2020, the GVP established Gayatri Vidya Parishad Institute of Higher Learning (GVPIHLR), a distinct university that promotes integration of Multidisciplinary, Interdisciplinary and Transdisciplinary fields. It enables the blend of ancient wisdom and traditional values with modern science and technology for the health and well-being of the society among the youth.

GVPIHLR will provide students with an integrative learning experience that transcends conventional disciplinary boundaries and offers abundant opportunities for academic exploration, innovation, and holistic development. It will foster multidisciplinary and transdisciplinary education and research through flexible curricular structures that enable students to creatively combine diverse disciplinary areas of study and pursue emerging areas of knowledge in multidisciplinary contexts.

Through this integrated academic ecosystem, GVPIHLR enable learners attain critical thinking, creativity, innovation, collaboration, and problem-solving, while drawing upon the strengths of diverse disciplines to address complex societal challenges and contribute meaningfully to the advancement of knowledge and the well-being of society.

SCHOOLS UNDER GVPIHLR:

- | | |
|--|--|
| ▪ School of Arts and Humanities | ▪ School of Commerce and Management |
| ▪ School of Chemical, Mechanical and Civil Engg. | ▪ School of Computer Science and Engg. |
| ▪ School of Electrical and Electronics Engg. | ▪ School of Sciences |

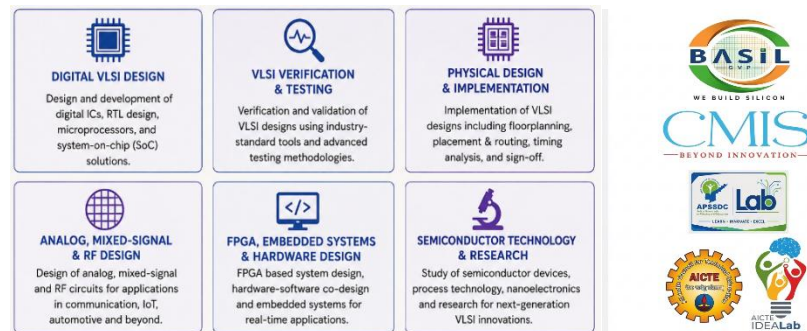
UG PROGRAMS UNDER SCHOOL OF ELECTRICAL AND ELECTRONICS ENGINEERING:

- B.Tech. in Electronics and Communications
- B.Tech. in Electronics Engineering (VLSI Design & Technology)
- B.Tech. in Electrical and Electronics
- B.Tech. in Electrical and Electronics (Electric Vehicles)

B.TECH. EE (VLSI DESIGN & TECHNOLOGY) AT A GLANCE

WHY EE(VLSI DESIGN & TECHNOLOGY)?

Electronics Engineering (VLSI) focuses on the design, development, and implementation of VLSI circuits and systems .It brings together electronics, semiconductor technology, VLSI design and Artificial Intelligence(AI) to createsmart, compact,high-performance solutions that power modern world.



PROGRAMME OBJECTIVES

- Build strong foundations in Electronics Engineering, VLSI Design, Semiconductor.
- Industry-relevant skills through project-based learning, internships, research, and emerging technologies.
- Foster innovation, critical thinking, entrepreneurship, and multidisciplinary collaboration.
- Develop socially responsible professionals committed to ethical engineering practicesand sustainable technological solutions.
- Prepare graduates for successful careers, higher studies, research, and lifelong learningin the rapidly evolving semiconductor and VLSI ecosystem.

PROGRAMME FEATURES

- | | | |
|---------------------|-------------------|-------------------------------------|
| • Professional Core | • Open Electives | • Choice Based Credit System (CBCS) |
| • Core Electives | • MOOCs / SWAYAM | • Multiple Entry / Multiple Exit |
| • Skill Courses | • Honors / Minors | • Industry based Internships |

CORE LEARNING AREAS

- Digital VLSI Design & RTL Design •Analog & Mixed-Signal IC Design •VLSI Design Verification & Validation
- Physical Design &Timing Analysis •Semiconductor Devices & Fabrication •FPGA & Embedded Hardware Design
- DFT & Low power VLSI .

CORE SKILLS: • Verilog HDL/ SystemVerilog/ UVM• RTL Coding & Simulation •VLSI Verification & Validation
•Logic Synthesis & Physical Design • FPGA Design & Prototyping

MODERN TOOLS & COMPUTATIONAL EXPOSURE

Industry Standard EDA Tools(Cadence/Synopsys/Siemens) sanctioned under **Chips to Start-up** (C2S) Programme from **Ministry of Electronics and Information Technology (MeitY)**•RTL Simulation & Logic Synthesis • Physical Design & Place-and-Route•FPGA Development •High-Level Synthesis & Hardware Acceleration•AI/ML for VLSI & Semiconductor

LEARNING BEYOND THE CLASSROOM

Hackathons & Design Challenges • Professional Society Activities • Technical Clubs & Student Chapters • Paper Presentations & Conferences

